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** CONTINUING DATA *None*****DV

** FOREIGN APPLICATIONS *****DV

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Verified and Acknowledged	<i>Shigeru</i> Examiner's Signature	JAPAN	33	20	3

ADDRESS

22850
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TITLE

Method of manufacturing semiconductor device capable of suppressing impurity concentration reduction in doped channel region arising from formation of gate insulating film

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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